

<b>Electronic Patent Application Fee Transmittal</b>				
<b>Application Number:</b>	10595157			
<b>Filing Date:</b>	09-Mar-2006			
<b>Title of Invention:</b>	METHOD OF MANUFACTURING MULTI-LAYER CIRCUIT BOARD			
<b>First Named Inventor/Applicant Name:</b>	Toshiaki Takenaka			
<b>Filer:</b>	Andrew Lawson Dunlap/Linda Deutscher			
<b>Attorney Docket Number:</b>	2006_0223A			
Filed as Large Entity				
<b>U.S. National Stage under 35 USC 371 Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
Submission- Information Disclosure Stmt	1806	1	180	180
<b>Total in USD (\$)</b>				<b>180</b>